



Surname \_\_\_\_\_

Other Names \_\_\_\_\_

Centre Number \_\_\_\_\_

Candidate Number \_\_\_\_\_

Candidate Signature \_\_\_\_\_

I declare this is my own work.

**A-level**

**PHYSICS**

**Paper 3**

**Section B    Electronics**

**7408/3BE**

**Friday 5 June 2020**

**Afternoon**

**Time allowed: The total time for both sections of this paper is 2 hours. You are advised to spend approximately 50 minutes on this section.**

**At the top of the page, write your surname and other names, your centre number, your candidate number and add your signature.**

**[Turn over]**



**For this paper you must have:**

- a pencil and a ruler
- a scientific calculator
- a Data and Formulae Booklet.

## **INSTRUCTIONS**

- Use black ink or black ball-point pen.
- Answer ALL questions.
- You must answer the questions in the spaces provided. Do not write outside the box around each page or on blank pages.
- If you need extra space for your answer(s), use the lined pages at the end of this book. Write the question number against your answer(s).
- Do all rough work in this book. Cross through any work you do not want to be marked.
- Show all your working.



**INFORMATION**

- The marks for questions are shown in brackets.
- The maximum mark for this paper is 35.
- You are expected to use a scientific calculator where appropriate.
- A Data and Formulae Booklet is provided as a loose insert.

**DO NOT TURN OVER UNTIL TOLD TO DO SO**



## SECTION B

Answer ALL questions in this section.

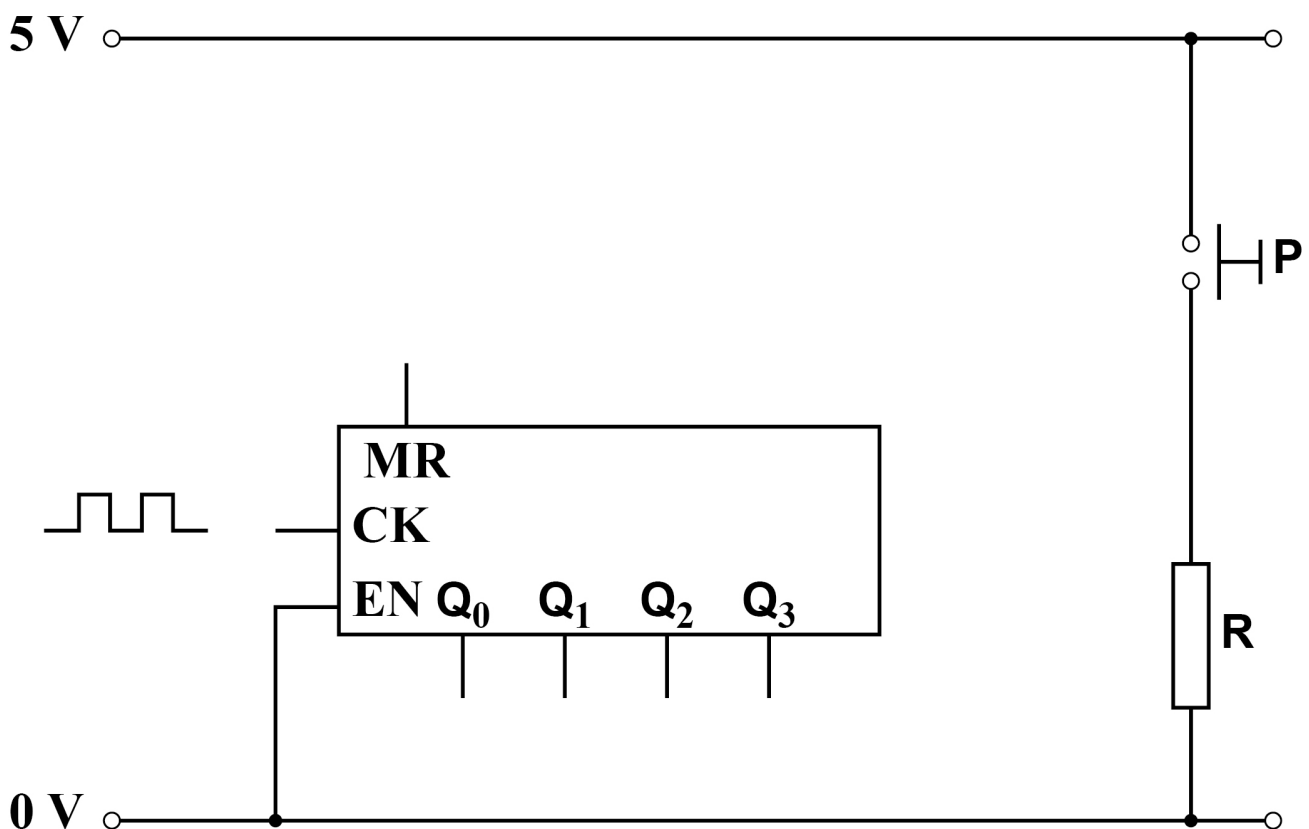
**01**

FIGURE 1 shows part of a circuit that includes a 4-bit binary counter. The main inputs and outputs of the counter are shown.

The counter generates a sequence of binary codes representing the decimal numbers 0 to 7

Output  $Q_0$  is the least significant bit of the binary codes.

FIGURE 1



**The counter resets when the master reset pin MR receives a logic 1**

**The circuit requires the counter to reset when either one of two conditions is met.**

**CONDITION 1**

**Manual reset using the switch P to reset the counter to 0**

**CONDITION 2**

**Automatic reset when an appropriate binary code is produced at the counter outputs. This will cause the counter to continually cycle through the decimal numbers 0 to 7**

**|   |   |
|---|---|
0	1

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1
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 Complete FIGURE 1 to show how both reset conditions can be met.**

**Do NOT show the power line connections to the integrated circuit. [3 marks]**

**[Turn over]**



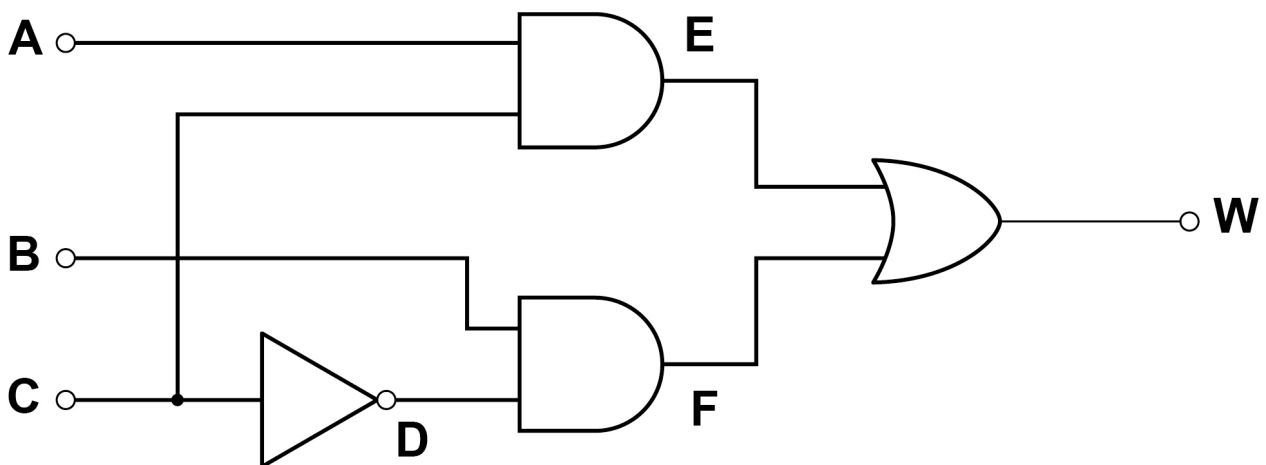
**01.2** A logic system is designed to identify prime numbers.

The binary codes from the counter are now applied to the inputs **ABC** of the logic system shown in **FIGURE 2**.

Input **A** takes the least significant bit of the binary code from the counter.

Output **W** becomes logic state 1 when a prime number 2, 3, 5 or 7 is detected. Otherwise output **W** is at logic 0

**FIGURE 2**



**Write the Boolean algebra expression for output W in terms of the inputs A, B and C.**

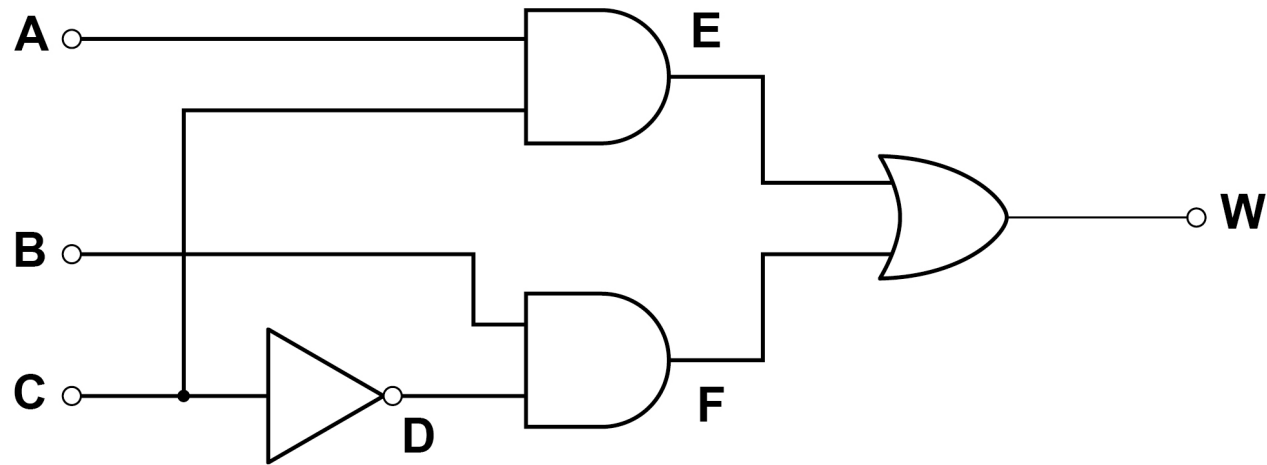
**The expression must contain only the four logic gate operations shown in FIGURE 2.  
[2 marks]**

**W = \_\_\_\_\_**

**[Turn over]**



## REPEAT OF FIGURE 2



**0 1 . 3** Complete TABLE 1, on the opposite page, the truth table for the logic system in FIGURE 2. [1 mark]



**TABLE 1**

<b>Decimal number</b>	<b>C</b>	<b>B</b>	<b>A</b>	<b>D</b>	<b>E</b>	<b>F</b>	<b>W</b>
<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>0</b>		<b>0</b>
<b>1</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>		<b>0</b>
<b>2</b>	<b>0</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>0</b>		<b>1</b>
<b>3</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>0</b>		<b>1</b>
<b>4</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>		<b>0</b>
<b>5</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>0</b>	<b>1</b>		<b>1</b>
<b>6</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>0</b>		<b>0</b>
<b>7</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>1</b>		<b>1</b>

**[Turn over]**



- 01.4** The logic system in Question 01.2 is replaced with one that gives an output S using the same binary input codes CBA.

The Boolean algebra equation for output S is

$$S = \bar{A} . ( B + C )$$

Deduce which decimal numbers 0 to 7 will cause S to become logic 1 [1 mark]

- 
- 01.5** Complete FIGURE 3, on the opposite page, by drawing the logic system for S.

You must use only the logic gate operations given in  $S = \bar{A} . ( B + C )$  [2 marks]



**FIGURE 3**

**A** ○ —

**B** ○ —

**C** ○ —

— ○ **S**

**[Turn over]**

<b>9</b>

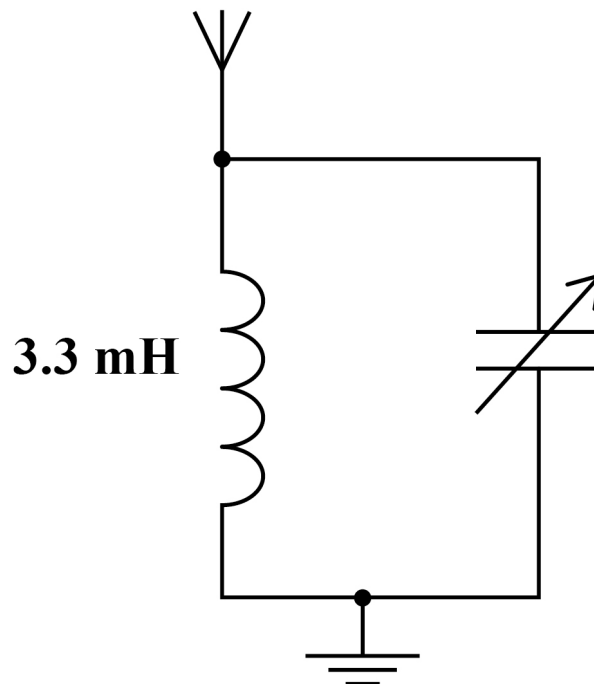


0	2
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**FIGURE 4** shows the filter circuit that forms the first stage in an amplitude modulated (AM) radio receiver.

The circuit contains a 3.3 mH inductor and a variable capacitor.

**FIGURE 4**



**02.1** The circuit is tuned to receive a radio station transmitting at a frequency of 1053 kHz.

**Calculate the value of the capacitance needed to receive this station. [1 mark]**

**capacitance = \_\_\_\_\_ pF**

**[Turn over]**



- 02.2** The circuit is retuned to receive a different radio station by setting the variable capacitor to a value of 9.3 pF.

**TABLE 2** shows the capacitance range of four variable capacitors W, X, Y and Z.

**Comment on the suitability of these capacitors for this application and state your preference. [2 marks]**

**TABLE 2**

Capacitor	Range / pF
<b>W</b>	<b>2–9</b>
<b>X</b>	<b>3–10</b>
<b>Y</b>	<b>4.5–20</b>
<b>Z</b>	<b>10–50</b>

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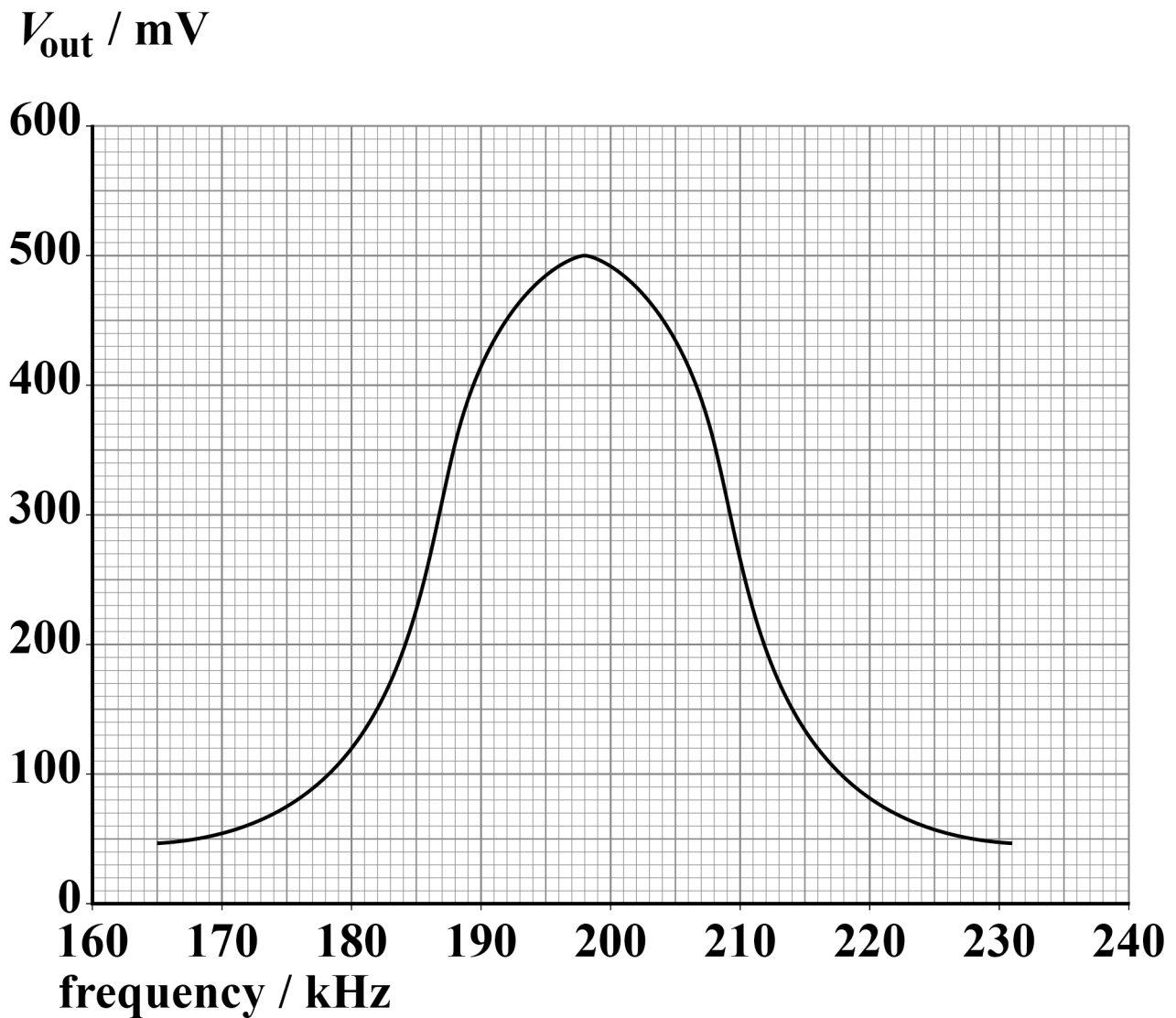
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**[Turn over]**



**02.3** FIGURE 5 shows part of the frequency response curve for a different filter circuit.

**FIGURE 5**





**Determine the bandwidth of the filter circuit.  
[2 marks]**

**bandwidth = \_\_\_\_\_ kHz**

**02.4 Calculate the  $Q$  factor of the filter circuit in  
Question 02.3. [1 mark]**

**$Q$  factor = \_\_\_\_\_**

**[Turn over]**



**0 2 . 5** The radio station is tuned using a different filter circuit with a very low  $Q$  factor.

**State and explain one effect of this change on the sound heard by a listener. [1 mark]**

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7



**03**

**Pulse code modulation (PCM) is used to encode live music as an uncompressed digital audio file.**

**Sampling of the analogue signal is carried out at 44.1 kHz.**

**A 16-bit system is used to encode each of the two channels that make up the stereo signal.**

**03.1**

**Explain why the sampling frequency used is suitable for this task. [2 marks]**

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**[Turn over]**



- 03.2** Calculate the number of quantisation levels available on a 16-bit encoding system.  
[1 mark]

number of quantisation levels = \_\_\_\_\_



**03.3** A recorded piece of stereo music lasts for 3.5 minutes.

**Calculate the size, in megabytes, of the digital file needed to store this recording. [2 marks]**

**file size = \_\_\_\_\_ megabytes**

**[Turn over]**



- 03.4** The music file is used by a call centre to play as background music while a phone call is on hold. However, the telephone network is designed to use a bandwidth of 0.3 kHz – 3.4 kHz.

**Compare the quality of the music heard by the telephone caller with that of the original file heard when played directly from a compact disc. [2 marks]**

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[Turn over]

7

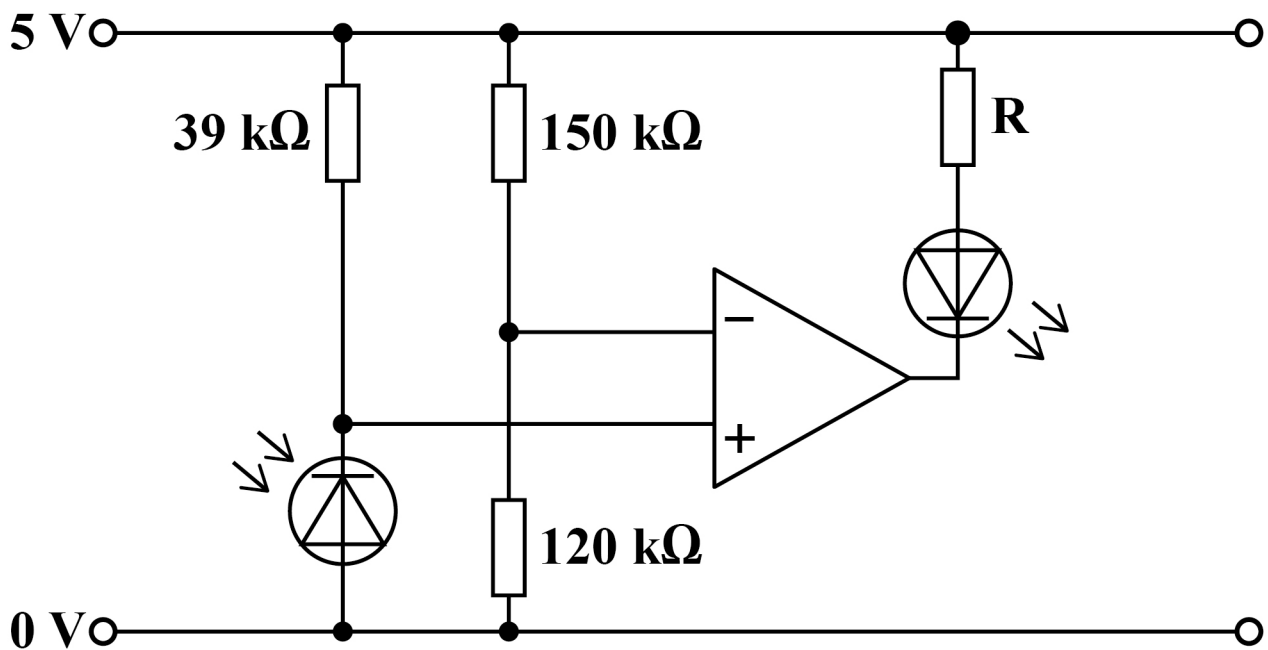


04

FIGURE 6 shows a circuit containing a photodiode and an ideal operational amplifier.

This circuit is used to monitor the intensity of monochromatic radiation.

FIGURE 6





**0 4 . 1** What is the configuration of the operational amplifier circuit shown in FIGURE 6?

Tick (✓) ONE box. [1 mark]

☐

**comparator**

☐

**differential amplifier**

☐

**inverting amplifier**

☐

**non-inverting amplifier**

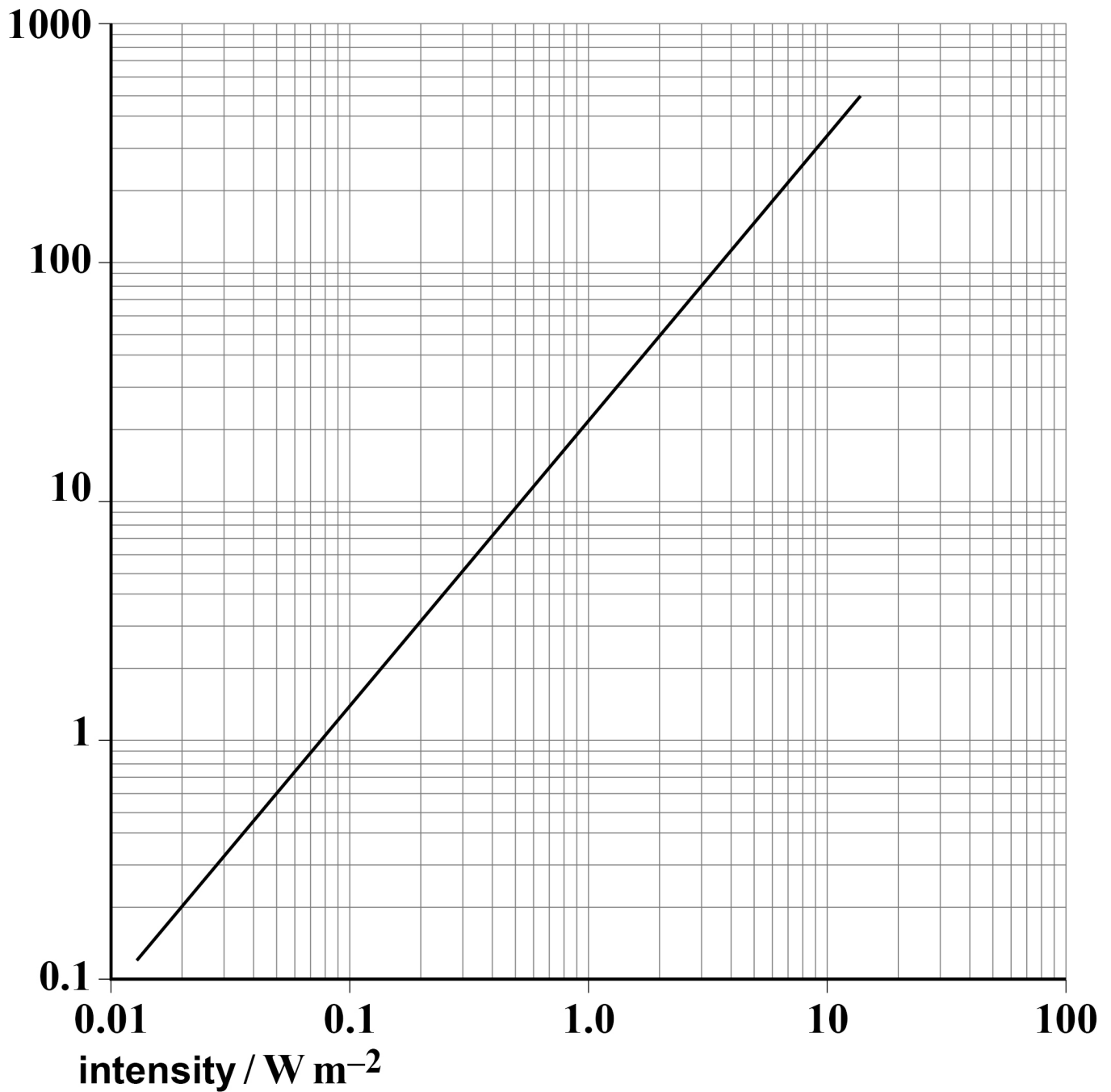
**[Turn over]**



**04.2** FIGURE 7 shows the variation of photocurrent with intensity for the monochromatic radiation incident on the photodiode.

**FIGURE 7**

photocurrent /  $\mu\text{A}$



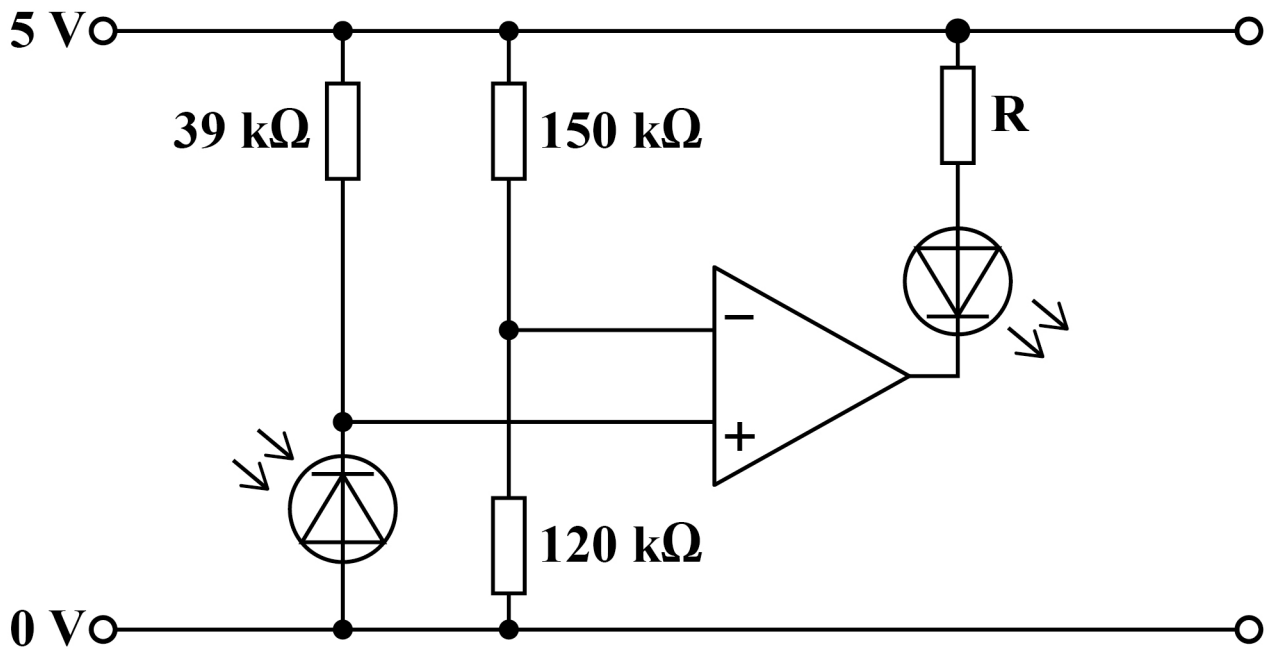
**Radiation of intensity  $3.0 \text{ W m}^{-2}$  is incident on the photodiode.**

**Show that the voltage at the non-inverting terminal ( $V_+$ ) of the operational amplifier is  $1.9 \text{ V}$ . [3 marks]**

**[Turn over]**



## REPEAT OF FIGURE 6



**04.3** The intensity of radiation incident on the photodiode remains at  $3.0 \text{ W m}^{-2}$ .

Deduce whether the light-emitting diode (LED) in FIGURE 6 is on or off. [2 marks]



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[Turn over]

6



0	5
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**British embassies in Europe are to be connected to a new long-distance communication link. The link, in the form of a land-based cable, will support multiple simultaneous video conferencing as well as the transmission of sensitive government data.**

**The company installing the link has to consider the choice between using optic fibre or copper wire in the cables.**

**Compare the advantages and disadvantages of the two options for use in these cables. State which option you would advise the company to use.**

**For both types of cable refer to their:**

- **physical properties**
- **ability to reject external interference**
- **signal-carrying properties.**

**[6 marks]**

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[illegible]



6



**Additional page, if required.**

**Write the question numbers in the left-hand margin.**

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**Additional page, if required.**

**Write the question numbers in the left-hand margin.**

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For Examiner's Use	
Question	Mark
1	
2	
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4	
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<b>TOTAL</b>	

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3 6



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